

FIG. 1

 $\left(\frac{\mathcal{S}_{i_{1}}^{-1}}{4n}\right)$

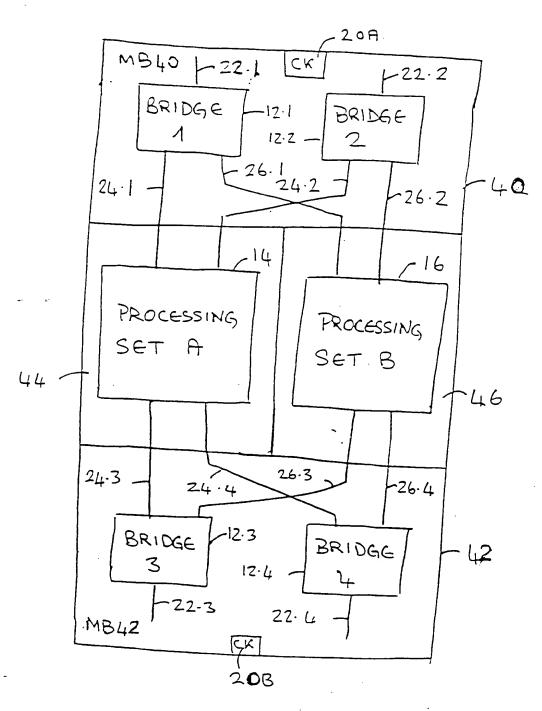


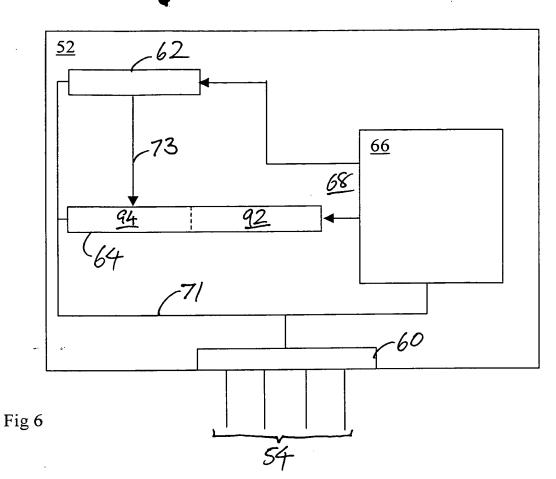
FIG.Z

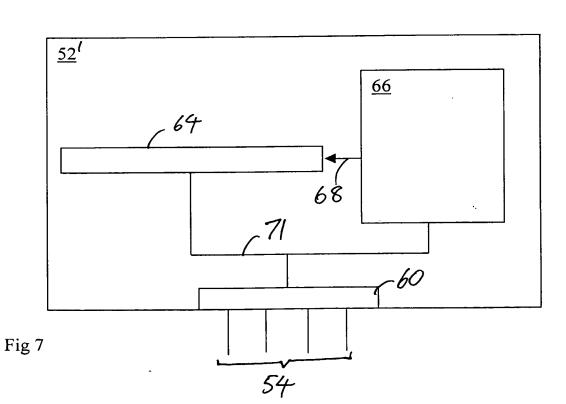
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Fig 4

D'4-	Field	Use	RW
Bits	Field	11 CONTRACTOR	R
<63:48>	manuf	Manufacturer identification	
<47:32>	impl	Implementation identification	R
<31:24>	mask	Mask set version	R
	Reserved		R
<23:16>	Reserveu		R
<15:8>	maxtl	Maximum trap level supported	
<7:5>	Reserved	-	R
<4:0>	maxwin	Maximum number of windows of integer register file.	R







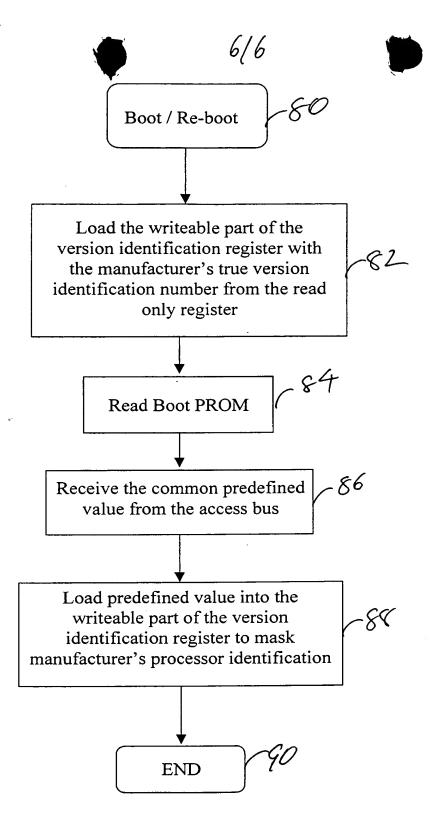


Fig 8